KOLHAPUR INSTITUTE OF TECHNOLOGY'S
COLLEGE OF ENGINEERING KOLHAPUR
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Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**One week workshop on Edge AI on Cortex Mx.**
Date	**23 to 27 July 2024**
Time	**10.00 AM to 3:30 PM**
Venue	**Seminar hall ETC Dept. or Lecture Theatre as per availability.**
Present Audience	**Students of ETC (T.Y.B.Tech. & Final Year)**
Report Author/s	**Prof.A.R. Nigavekar**
Resource Person	**Ms. Mrinalini Barik ,Senior Application Engineer DigiToad Technologies Pvt. Ltd.**

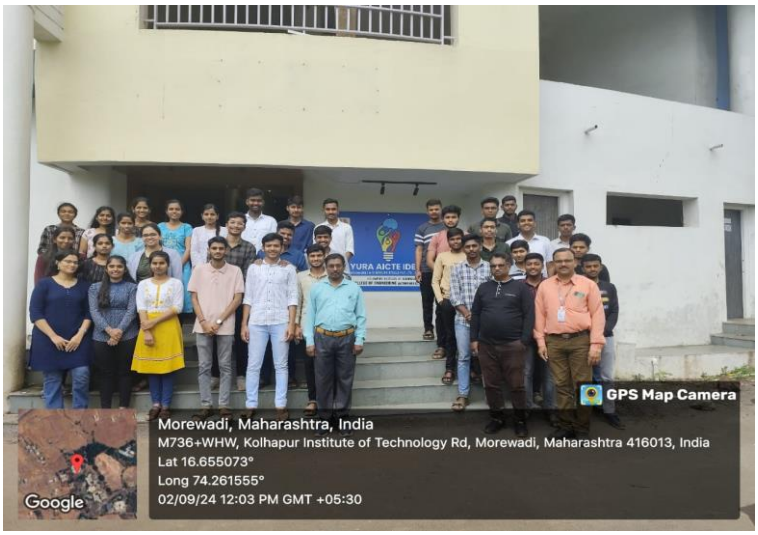
Objectives of the Event:

1) Study Cube IDE of ST Microelectronics.
2) Develop projects to study on chip resources of CortexM4 microcontroller.
3) Implement AI based project on Cortex M4 Microcontroller.

Workshop was conducted from 23 to 27 July 2024 by Ms. Mrinalini Barik, Senior Application Engineer, DigiToad Technologies Pvt. Ltd.

Outcome: Activity engaged and educated the participants about the implementation of edge AI projects on microcontroller.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Three days' workshop on "Circuit Simulation and PCB Design".**
Date	**31 August to 02 September 2024**
Time	**10.00 AM to 3:30 PM**
Venue	**ETCR01, ETCR02 & IDEA LAB.**
Present Audience	**Students of ETC (2rd Year)**
Report Author/s	**Dr.A.L.Renke**
Resource Person	**Dr.A.L.Renke & Mr.A.S.Mohite**



"Circuit simulation and PCB design" a three day workshop was arranged for second year division A and B students of E & TC department in association with Mayura AICTE idea lab, IETE and Electronics and Telecommunication department on August 31, 1 and 2 September 2024.

In this workshop students are benefited with Introduction to ORCAD, Types of Simulation Creating a Schematic Design, Simulating a Design in ORCAD and in PCB designing students are benefited with TinkerCAD, Arduino programming, Introduction to Eagle CAD, Circuit Design in Eagle CAD, PCB Design in Eagle CAD, generation of Gerber files in copper CAM software, Generation of G-codes and fabrication of PCB on PCB milling Machine.

Tools and software used for workshop: ORCAD 9.2 and PSPICE, TinkerCAD, Arduino programming, Eagle CAD 7.5.0, Copper CAM, Mach3, PCB miling machine, soldering and desoldering station.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Guest Lecture on Higher Studies.**
Date	**10 September 2024**
Time	**2.00 PM**
Venue	**Seminar Hall of ETC Department**
Present Audience	**Students of ETC (2nd and 3rd Year)**
Report Author/s	**Mrs.M.S.Shikalgar, Dr.P.B.Nikam**
Resource Person	**Mrs. Pallavi Desai** **Achievers Academy, Kolhapur.**

Department of Electronics & Telecommunication Engineering of KIT college of engineering, Kolhapur,& IETE Sub centre Kolhapur have jointly organized Guest Lecture on “Opportunities in Abroad for Higher Studies” on 9th October, 2024. The session was conducted by the expert Mrs. Pallavi Desai MBA (HR & Marketing), M.Phil (Business Administration) , Achiever’s Academy.

In this session, more than 50 students from 2nd Year and 3rd Year E &TC students were participated. Mrs. Pallavi Desai has shared her experiences on preparation of GRE, TOFEL, IELTS exams. And also the application process for USA, Canada, Germany and other countries. She has discussed regarding the eligibility criteria and profile building activities, expenses and financial planning for higher studies in abroad. She also discussed regarding the scholarship, assistantship and other forms of financial aid and what kind of master studies we can do after B.Tech such as M.S, MBA, IIM, M.Tech. Main focus of the talk was to make students aware about the Higher education in abroad. Overall, students were very impressed from this expert lecture.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Workshop on "Advanced Microcontroller Using Texas Instrument Kits".**
Date	**26 October 2024**
Time	**09:30 AM to 4:30 PM**
Venue	**ETCR01**
Present Audience	**Students of ETC (2rd Year)**
Report Author/s	**Mr. Khandare V.V.**
Resource Person	**Mr. Niraj Kapase, Senior Technical Lead, Brose Automative India Ltd. Pune**

Modern day Embedded Systems curriculum requires an application and Systems Design approach balancing the low power needs, performance, connectivity requirements and system cost. This Texas Instruments kits is designed to inculcate this perspective in the students while introducing them to Microcontrollers using MSP430, an industry standard hardware platform. The kits help usto understand 16-bit architecture and its programming considerations using C language. Later part is focused on programming various inbuilt features of the platform with more focused approach on analog and digital interfacing concepts and related protocols. The

considerations in keeping the System power consumption low are addressed along the way. Embedded systems whether they are standalone or networked need various communication interfaces and standards so that they communicate and process data from external sensors and actuators. It will cover how to connect the device to external peripherals including those needed for internet connectivity.

As the need of industry base knowledge is the prime requirement for now a day Engineering students. Keeping this view in mind Kolhapur Institute of Technology College of Engineering, Kolhapur Is striving hard to give lot of project based and industry based training to students by arranging different workshops and training for students. Under the banner of IETE, students of S.Y E& TC got benefited byacquiring skills and hands on training given by industry expert Mr. Niraj Kapase, Senior Technical Lead, Brose Automative India Ltd.,Pune

Resource person gave detail introduction about usability of Texas instruments kits in industries. He gave the detail architecture and specifications of MSP430 launch pad. The actual coding and simulation by Energia and code composer was demonstrated by him. Installation of software and hardware implementation was demonstrated by him in detail. He encourage the students to develop mini projects using Texas Instruments kits and also insist to show case their talent by participating in different competition. Session was concluded by taking quiz for students which was more interesting and enjoyable. Students enjoyed the learning from industry expert and got confidence to handle the Texas instrument kit successfully. Session ended with vote of thanks to coordinator and resource person by Mr.Khandare V.V.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Guest Lecture on- "Guidelines while appearing for GATE-2025 examination".**
Date	**12 November 2024**
Time	**3.00 pm onwards**
Venue	**ETCR 01 of ETC Department**
Present Audience	**Students of ETC (3rd Year and Final Year)**
Report Author/s	**Mrs. Minaj Shiklagar and Dr. P.B.Nikam**
Resource Person	**Dr. Ashish A. Patil** **Dept. of CSE-AIML, KIT's COEK.**

The Department of E&TC organized an insightful guest lecture on the GATE (Graduate Aptitude Test in Engineering - 2025) examination to help students understand the importance of this exam in shaping their career paths. The lecture was aimed at providing information on effective preparation strategies, insights into the exam pattern, and its significance for pursuing higher education and job opportunities in the public sector.

Key Highlights

1. Introduction to GATE Examination: The speaker gave a detailed overview of the GATE exam, including its structure, marking scheme, and eligibility criteria. He explained how this exam serves as a gateway for higher studies and government-sector jobs.
2. Preparation Strategies: The speaker shared tips on how to plan an effective study schedule, the importance of time management, and the necessity of understanding the weightage of different topics. The emphasis was on consistent practice, understanding concepts, and managing exam pressure.
3. Resources and Materials: This guest lecturer recommended various study resources such as standard textbooks, online courses, and mock tests. He encouraged students to follow a structured approach by leveraging quality resources to improve their preparation.
4. Career Opportunities After GATE: The speaker explained various career options available to students post-GATE, including M. Tech and Ph.D. admissions in premier institutions like IITs and NITs. He also discussed job opportunities in government organizations and PSUs, highlighting the benefits of working in these sectors.
5. Q&A Session: The session ended with a Q&A segment where students asked questions related to specific subjects, exam anxiety, and balancing college studies with GATE preparation. The speaker provided practical tips and solutions for each question, enhancing the session's value.

Feedback from Students-

The lecture was highly appreciated by the students, who found it to be informative and motivating. They gained a clearer understanding of the GATE examination and felt more confident about their preparation strategy.

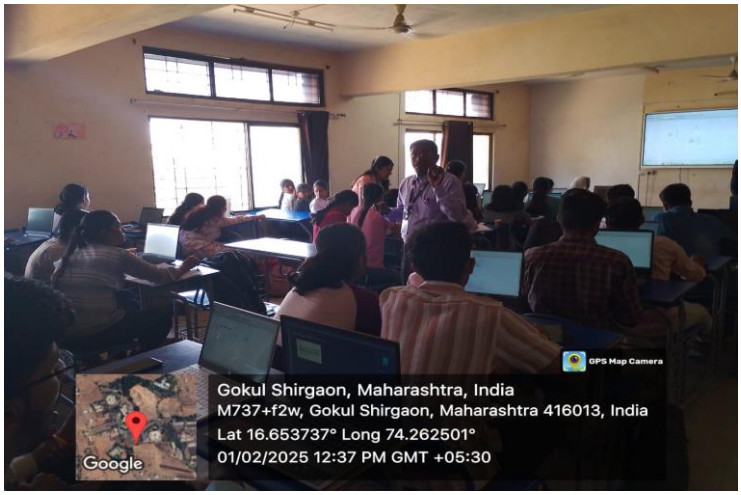
Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**CLPBL Day**
Date	**28th November 2024**
Time	**09:30 AM to 4:30 PM**
Venue	**Antenna and microwave Lab, Communication Engg-I Lab and Power Electronics Lab.**
Present Audience	**Students of ETC (2nd , 3rd Year & Final Year)**
Report Author/s	**Prof.A.R.Nigavekar, Dr.S.S.Nagtilak, Mr.V.K.Desai, Mr. V.B.Gundavade, Mrs.M.V.Gangapure, Mrs.V.A.Suryawanshi**
Resource Person	**Dr.M.S.Patil, Head, ETC DEPT, RIT, Sakharale, Islampur Dr.S.S,Powar, BVCOE, Kolhapur**

Separate topics based on concept of Digital System Design (SY B.Tech.), Model Based Design (TY B.Tech.) and Computer Communication Network (Final Year B.Tech.) projects were assigned to all students in groups (maximum 4-6 students per group) to enable healthy competition among the different teams. The students work in groups and assign and distribute various aspects of work so as to realize the project based on a timeline of about 2 to 3 months. Queries and doubts are clarified by interactions with the PBL coordinators and subject experts. Student groups submit the PBL report during their demonstrations on a specified date in front of the faculty members. All ETC Engineering Faculty of the concerned class was judges for PBL demonstration. Three projects from each class were selected for final presentation.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Parent Teacher Meet**
Date	**30 November 2024**
Time	**10:00 AM to 1.00 PM**
Venue	**Seminal Hall of E&TC Department**
Present Audience	**Parents of ETC DEPT (2nd, 3rd Year and Final Year)**
Report Author/s	**Ms. S. R. Lad**
Resource Person	**Parent Teacher Meet Coordinator: Ms. S.R.Lad** **Anchoring: Mrs. M.V.Gangapure** **Welcome speech and Department Information: Dr.Y.M.Patil** **Special address: Dr. Akshay Thorvat (Dean Academic, KITCOEK)** **Department Activities: Dr.M.D.Sontakke** **Vote of thanks: Mr. A.S. Mohite**

In the ETC department, parent's meet was held with the main motive of conveying the parents the progress of their respective wards. The meet began with welcome speech and department information by HOD. After this, Dr. Akshay Thorvat (Dean Academic) gives information about co-curricular activities. Dr.M.D.Sontakke gives information about different activities arranged in the department. In addition to this, the first three toppers of each class as well as students who are secured ranks in co-curricular activities were felicitated. The meet indeed was a great opportunity where parents, faculty members and students came together and had a good interaction. Finally vote of thanks is delivered by Mr. A.S.Mohite. Parent Teacher Meet was coordinated by Ms. S.R.Lad.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**One day workshop on "Proteus/ORCAD Simulation and PCB design"**
Date	**01 February 2025**
Time	**09:30 AM to 4:30 PM**
Venue	**ETCR-01, ETCR-02**
Present Audience	**Students of ETC (2nd Year)**
Report Author/s	**Dr.A.L.Renke**
Resource Person	**Dr.A.L.Renke , Mr.A.S.Mohite & Mr.Prasad Gaikwad**



"Circuit simulation and PCB design" a three day workshop was arranged for second year division A and B students of E & TC department in association with IETE and Electronics and Telecommunication department on Saturday, 1st February 2025 In this workshop students are benefited with Introduction to ORCAD, Types of Simulation

Creating a Schematic Design, Simulating a Design in ORCAD/PROTEUS and in PCB designing students are benefited with, Introduction to Eagle CAD, Circuit Design in Eagle CAD, PCB Design in Eagle CAD, generation of Gerber files in copper CAM software, Generation of G-codes.

Tools and software used for workshop: ORCAD 9.2 and PSPICE, Eagle CAD 7.5.0, Copper CAM, Mach3.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**C-Coder Clash**
Date	**22 February 2025**
Time	**09:30 AM to 4:30 PM**
Venue	**ETCR-01, ETCR-02**
Present Audience	**Students of our and other Institutes**
Report Author/s	**Dr.S.S.Nagtilak**
Judge for the event	**Mr. Muralidhar Parulekar, CTO of Manorama Info Solutions, Kolhapur**

Objective of the event: The C-Coder Clash event aims to provide a platform for coding enthusiasts to showcase their programming skills in the C language. It encourages problem-solving, logical thinking, and algorithmic efficiency while fostering a competitive yet collaborative environment. The event also seeks to enhance participants' coding proficiency, speed, and accuracy through real-world challenges.

Outcome of the event:

Skill Enhancement: Participants improved their coding skills, logical reasoning, and problem-solving abilities.

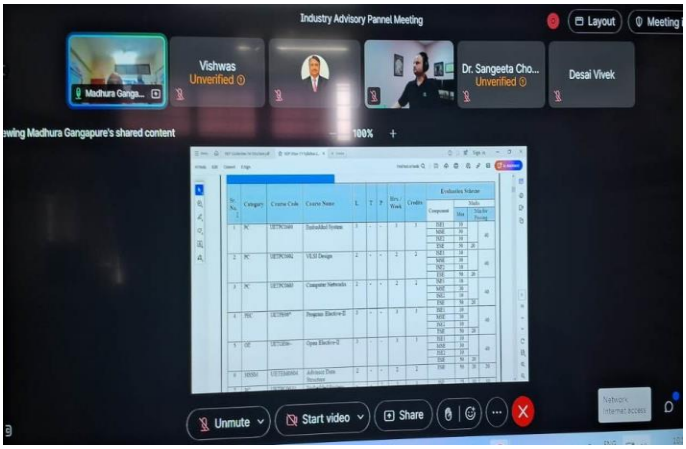
Competitive Spirit: The event fostered healthy competition, pushing coders to think critically and perform under pressure.

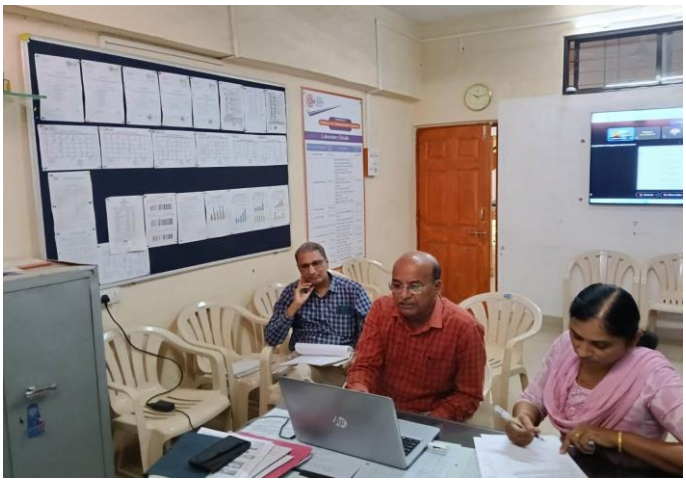
Knowledge Sharing: Contestants gained insights into different approaches and optimized solutions through discussions and analysis.

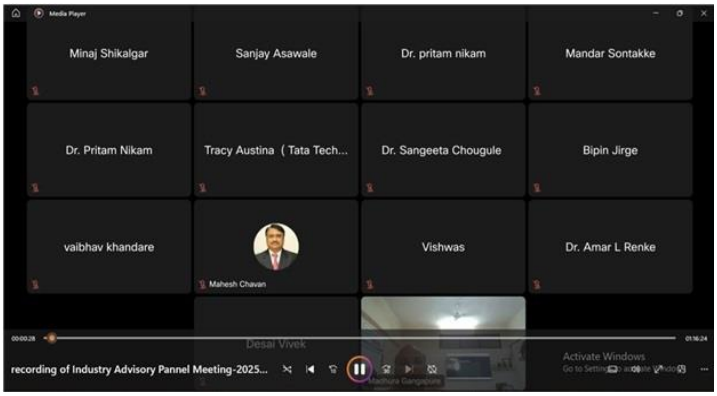
Recognition & Rewards: Top performers were acknowledged and rewarded, boosting motivation and encouraging further learning.

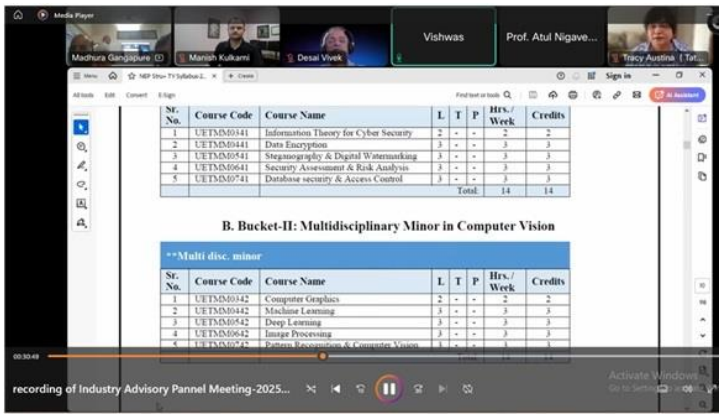
Networking Opportunities: Participants interacted with fellow coders, mentors, and industry professionals, expanding their connections.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Industry Advisory Panel (IAP) meeting**
Date	**05 April 2025**
Time	**10.00 AM to 12:30 PM**
Venue	**HOD Cabin and IAP members are in online mode.**
Present Audience	**All faculty members**
Report Author/s	**Dr.M.D.Sontakke**
Resource Person	**BOS Members**









Agenda for IAP meeting was, to discuss and finalize proposed content-wise syllabus of T.Y.B.Tech. from AY 2025-26. Meeting was conducted in Online mode.

Mr. Bipin Jirge, Mr.Chandrakant Lokulwar, Mr. Pralhad Dhumal, and Mrs. Shambhavi Kulkarni given the suggestions for some courses through email.

Other members who were present in online mode are,

1) Mr. Bipin Jirge
2) Ms. Tracy Austina
3) Mr. Pralhad Dhumal
4) Mr. Sanjay Asawale
5) Mr. Abhijit Desai
6) Mr. Manish Kulkarni

Considering suggestions of above members, we finalize TY B.Tech. content-wise syllabus.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Guest Lecture on Dr.J.C.Bose life and work.**
Date	**05 April 2025**
Time	**09:30 AM to 11:30 AM**
Venue	**ETCR 02**
Present Audience	**Students of ETC (3rd Year)**
Report Author/s	**Mr.E.C.Patil**
Resource Person	**Dr. Pramod Vasambekar**

Guest Lecture on “Dr. J. C. Bose : Life & Work” organized by IETE sub centre department of E&TC, On 5th April 2025. IETE Sub center Secretary Dr. Y. M. Patil felicitated Dr. Pramod Vasambekar by offering bouquet. Lecture is organized for TY E&TC students & IETE treasure Dr. S. R. Chougule & Dr. Amar Renke Idea Lab coordinator were present. Dr. Pramod Vasambekar started lecture at 9.30 am & given information regarding above chapter as follows. Professor Jagadis Chunder Bose, popularly known as J. C. Bose, did pioneering work in millimeter-wave communication in 1890s. After completing higher education in England, Prof. Bose served at Presidency college, Kolkata as a professor of physics. He started his research career with work on ‘electric radiation’. He was not having an established laboratory, apparatus or peers. He developed many millimeter-wave components required in his research work. He demonstrated the transmission and reception of electromagnetic waves at 60 GHz to the public at Kolkata. He invented detectors for detection of electromagnetic waves and patented ‘Detector for Electrical Disturbances’ on March 29, 1904 (United States Patent Office No.755, 840). In this patent he reports the use of semiconductor-galena for detection of electromagnetic waves. His two more British patents have been recently discovered. In later part of his life he worked a lot on plant physiology. He presented his research work at Royal Institution, London and other places. His work is published in national /international journals. He authored many books. He is the “Founder of modern science in the Indian subcontinent”. He is the founder of Bose Institute Asia's first modern research centre devoted to interdisciplinary research. He is honored with many awards / recognitions. This presentation is an effort to review his work at millimeter wavelength in electromagnetic.

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**Electro Mastery 2.0**
Date	**9 ,10,12,13 April 2025**
Time	**03:30 PM onwards**
Venue	**Seminar Hall of the Department**
Present Audience	**Students of ETC DEPT (1st, 2nd, 3rd Year)**
Report Author/s	**Ms. A. S. Nille**
Resource Person	**Guest Session: Mr.Psasad Tipugade** **Students of Innovation Club**

Academic Year 2024-25	
Department of Electronics & Telecommunication	
Activity	**CLPBL Day**
Date	**26th April 2025**
Time	**09:30 AM to 4:30 PM**
Venue	**Antenna and microwave Lab, Communication Engg-I Lab and Power Electronics Lab.**
Present Audience	**Students of ETC (2nd and 3rd Year)**
Report Author/s	**Mr.V.V.Khandare and Mrs.V.A.Suryawanshi**
Resource Person	**Mr. Atul B. Jadgale, Jet Industries, Ichalkaranji**

Mini Project-II and Mini Project-III are the identified course for SY and TY BTECH respectively for CLPBL.Projects were assigned to all students in groups (maximum 4-6 students per group) to enable healthy competition among the different teams. The students work in groups and assign and distribute various aspects of work so as to realize the project based on a timeline

of about 2 to 3 months. Queries and doubts are clarified by interactions with the PBL coordinators and subject experts. Student groups submit the PBL report during their demonstrations on a specified date in front of the faculty members. All ETC Engineering Faculty of the concerned class was judges for PBL demonstration. Three projects from each class were selected for final presentation.